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•	Application No.	Applicant(s)		
Mading of Allowed 1994	09/926,377	GRASSL, THOMAS		
Notice of Allowability	Examiner	Art Unit		
	Laura M. Schillinger	2813		
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.				
1. \square This communication is responsive to <u>8/1/2005</u> .				
2. X The allowed claim(s) is/are 19-26.				
3.				
 Attachment(s) 1. ☒ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 8/1/2005 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	5. Notice of Informal P 6. Interview Summary Paper No./Mail Da 7. Examiner's Amendr 8. Examiner's Stateme 9. Other	(PTO-413), te ment/Comment		

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DETAILED ACTION

Allowable Subject Matter

Claims 19-26 are allowed.

The following is an examiner's statement of reasons for allowance: The arguments filed in Applicant's appeal brief dated 5/17/05 are deemed persuasive. Specifically, Applicant argues, "[T]here is a significant difference between having to build up multiple layers of silicon and insulating material before any formation of electrical connections can begin, as disclosed by Finnila, as opposed to simply providing one of the first layers of the device (the first substrate) with active circuit components to define a first circuit layer as recited. Further, the first circuit layer (active circuit component) formed in Finnila is actually gate electrode 18 which is formed on top of first substrate 12 (see FIG. 5; col. 4, lines 28-30), but not along and within at least one portion thereof as recited. Depositing a circuit component on top of a first substrate to define a first circuit layer as disclosed by Finnila is not equivalent to the circuit component being along and within at least one portion of the first substrate to define a first circuit layer as recited." (page 13, Appeal Brief). This is determined to be persuasive. Consequently, claims 19-26 are deemed to be allowable.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Laura M. Schillinger whose telephone number is (571) 272-1697. The examiner can normally be reached on M-T, R-F 7:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl W. Whitehead, Jr. can be reached on (571) 272-1702. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Saecea Mylley
04/26/06

Laura M Schillinger Primary Examiner Art Unit 2813